

S/N 10/615,891

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Vishnu K. Agarwal  
Serial No.: 10/615,891  
Filed: July 8, 2003  
Customer No.: 73115  
Title: STRUCTURES AND METHODS FOR IMPROVED CAPACITOR CELLS IN  
INTEGRATED CIRCUITS

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Examiner: Edward Wojciechowicz  
Group Art Unit: 2895  
Docket No.: 303.628US2  
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**COMMUNICATION RE: FEE ADDRESS**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

In response to the Notice of Allowance and Issue Fee Due, please record the Fee Address under the provisions of 37 CFR 1.363 as the following:

**Customer Number 26809**

which is associated with Micron Technology, Inc..

Please direct any inquiries to the undersigned attorney at (612) 373-6969.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 8 day of January, 2009.

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